

APPARATUS AND METHOD FOR GENERATING A PHASE DELAY

Abstract

An apparatus for generating a phase delay is disclosed. The apparatus includes a buffer utilized for buffering an input signal and then outputting an output signal; a digital to analog converter (DAC) utilized for converting a digital value representative of phase delay into a corresponding control voltage and outputting a control voltage; and a variable capacitor that has a capacitance value controlled by the control voltage. By controlling the variable capacitance value, the apparatus for generating a phase delay can adjust the phase delay between the input signal and the output signal.